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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10056054	01/28/2002	438	795	2812	Pham

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**CONTINUING DATA VERIFIED:
NO

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-019331 01/29/2001
yePG-PUB DO NOT PUBLISH ☐RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

0756-2427

TITLE : Method for manufacturing a semiconductor device

U.S. DEPT. OF COM. / PAT. & TM-PTO-436L (Rev. 12-54)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	(Print Claim for O.G.)
Assistant Examiner		DRAWING	
		Sheets Draw	Figs Draw (Print Fig)
Amount Due		Application Examiner	
Date Paid			
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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